

**BRIEF DESCRIPTION OF AGRUMENTS TO BE PRESENTED:**

It should also be noted that claimed invention provides interpreter code that copies native code corresponding to "just-executed" (i.e., interpreted) code into a code cache when a branch (Bytecode) instruction is encountered. Please note that the invention aims at avoiding Just-in-time (JIT) compilers by "adding interpreter code which labels code copied into the cache." (specification, page 5, line15 to page 6-line 5) Please note the proposed claim 1.

It is noted that Walters states that a branch instruction (B) flag can be set to true only for qualifying instructions that are branch instructions (Walters, Col. 10, line 25-27). It is further noted that a cross-compiler of Walters performs additional processing so as to minimize the native code instructions generated for non-native code conditional branch instructions by minimizing the number of native code instructions used to handle non-native condition codes. For instance, the cross-compiler of Walters determines whether a conditional branch instruction is the target of any other branch instruction(s) within a compilation window and whether it is immediately preceded by a comparison instruction. If the conditional branch instruction is the target of another branch instruction within the compilation window (or is an entry point instruction), and is immediately preceded by a comparison instruction, the cross-compiler converts the non-native comparison instruction into native instructions for generating and storing non-native condition codes, and converts the conditional branch instruction into a sequence of instructions for determining the status of the relevant non-native condition codes prior to performing a conditional branch (Walters, Col. 5, line 10-30).

Accordingly, it is respectfully submitted that Walters does NOT teach interpreter code (logic implemented into the interpreter itself) that copies native code corresponding to "just-executed" (i.e., interpreted) code into a code cache when a branch (Bytecode) instruction is encountered, and executes code from the code cache when it encounters a branch instruction. As such, it is respectfully submitted Adams does NOT teach the recited operations of claim 1 (methodology depicted in Fig. 3).

An interview was conducted on the above-identified application on

\*Note: This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP §713.01). This application will not be delay from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFT 1.33(b)) as soon as possible.



(Applicant/Applicant's Representative)  
Signature)

(Examiner/SPE Signature)